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Information Disclosure Statement By Applicant	Applicant: John R. Chase	
(Use Several Sheets if Necessary)	Filing Date October 23, 2003	Group 2123 2128

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A1						
	A2						
	A3						
	A4						
	A5						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	B1							
	B2							
	B3							
	B4							
	B5							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
SL	C1	Hutton et al., "The Circuit Characterization and Generation Project at the University of Toronto", www.eecg.toronto.edu/~mdhutton/gen/ downloaded September 8, 2003
SL	C2	Synopsys Products & Solutions, Design Platforms, www.synopsys.com/products/products.html site modified August 1, 2003, downloaded September 8, 2003
	C3	
Examiner		Date Considered 3/8/06

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.